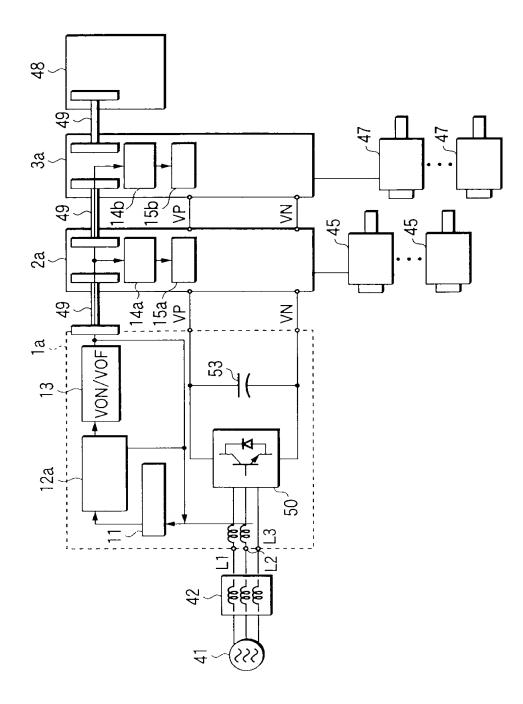
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FIG.2(a)

INPUT PHASE-TO-PHASE VOLTAGE VAC

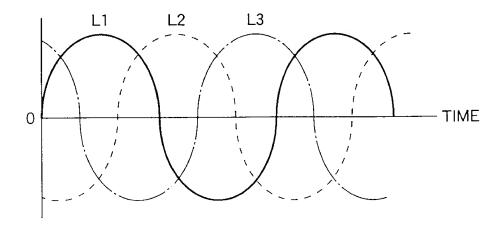


FIG.2(b)

INPUT CURRENT II

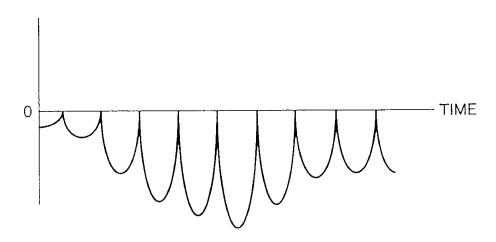


FIG.2(c)

INPUT CURRENT DETECTION PERIOD (CONTROL PERIOD)



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FIG.3(a)

INPUT CURRENT Ii

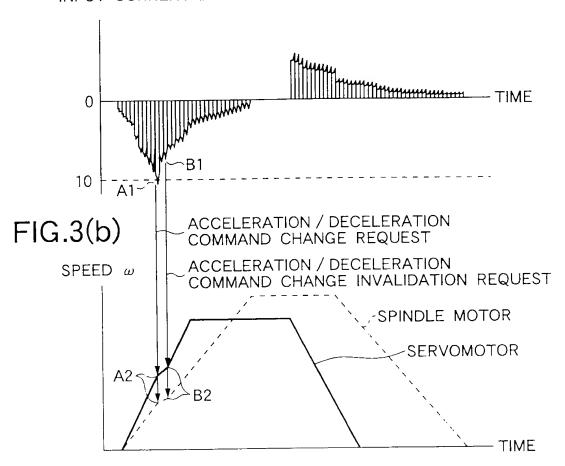
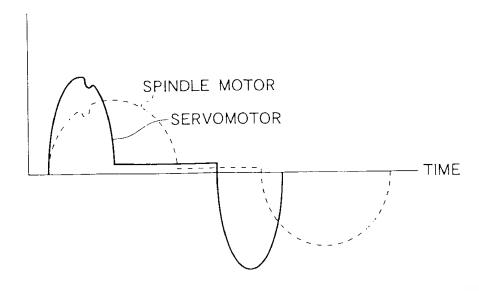


FIG.3(c)

DRIVE CURRENT Id



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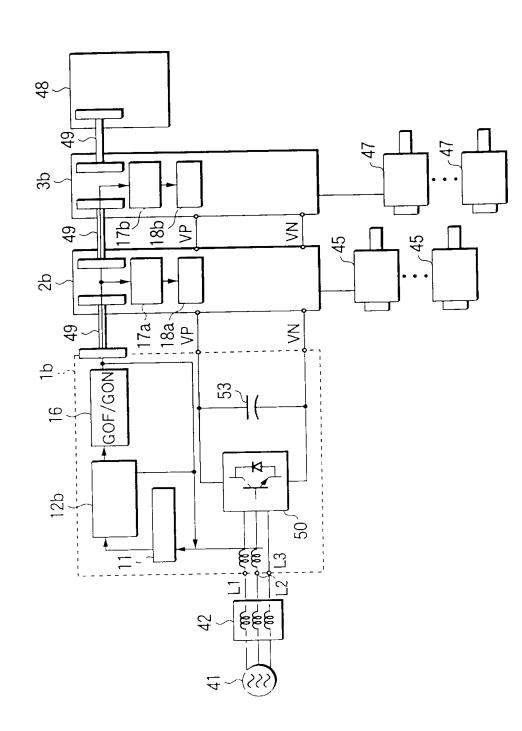


FIG.5(a)

INPUT CURRENT Ii

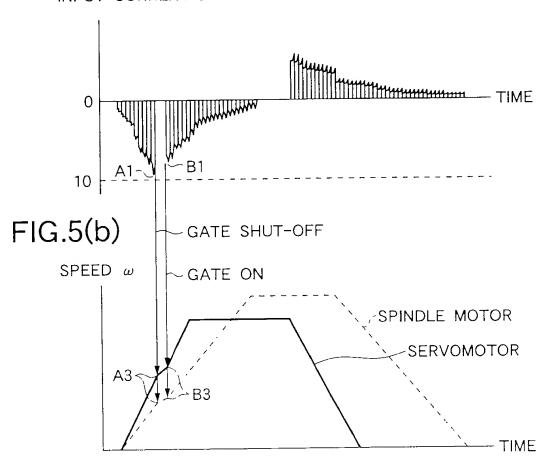
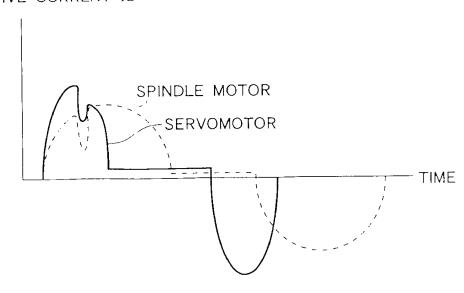
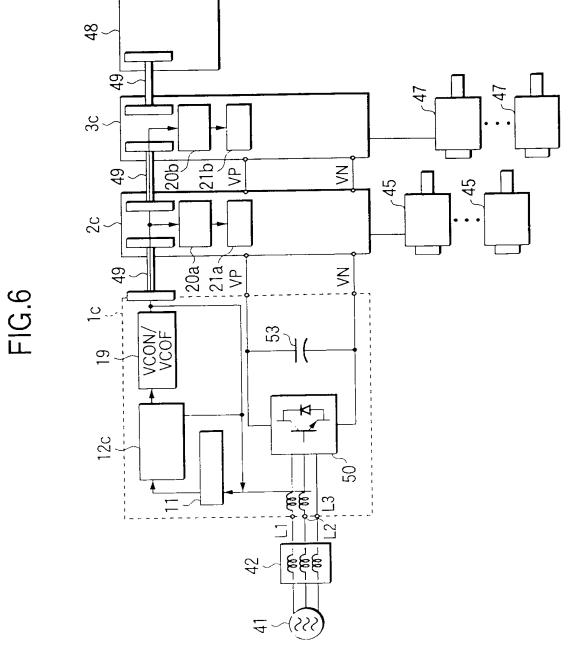


FIG.5(c)

DRIVE CURRENT Id





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FIG.7(a)

INPUT CURRENT Ii

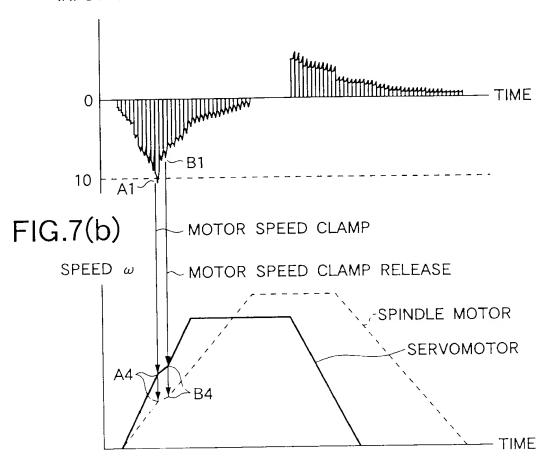
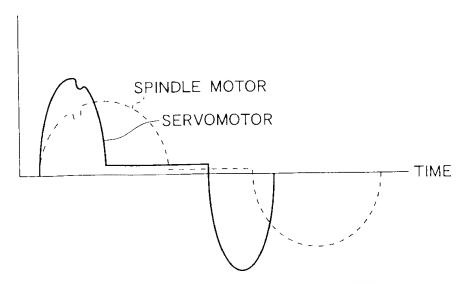


FIG.7(c)

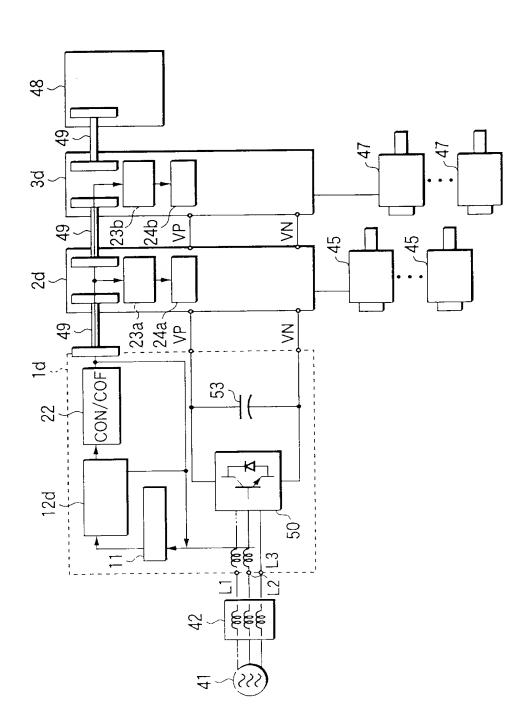
DRIVE CURRENT Id





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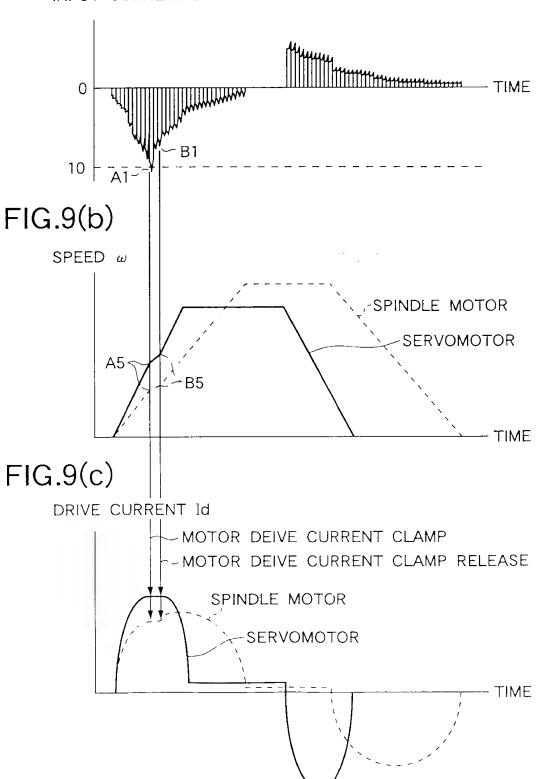


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FIG.9(a)

INPUT CURRENT II



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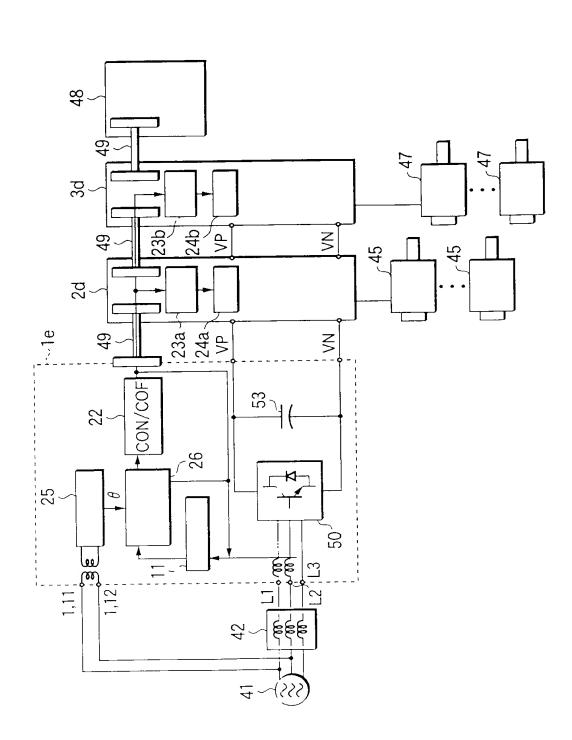


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FIG.11(a)

INPUT PHASE-TO-PHASE VOLTAGE VAC

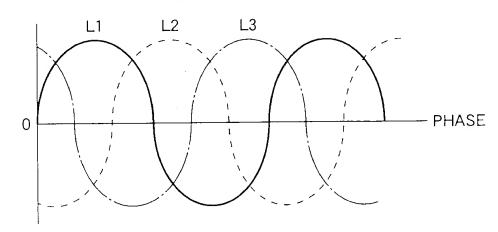
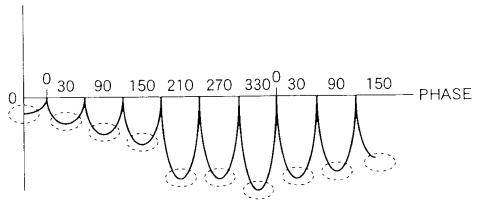


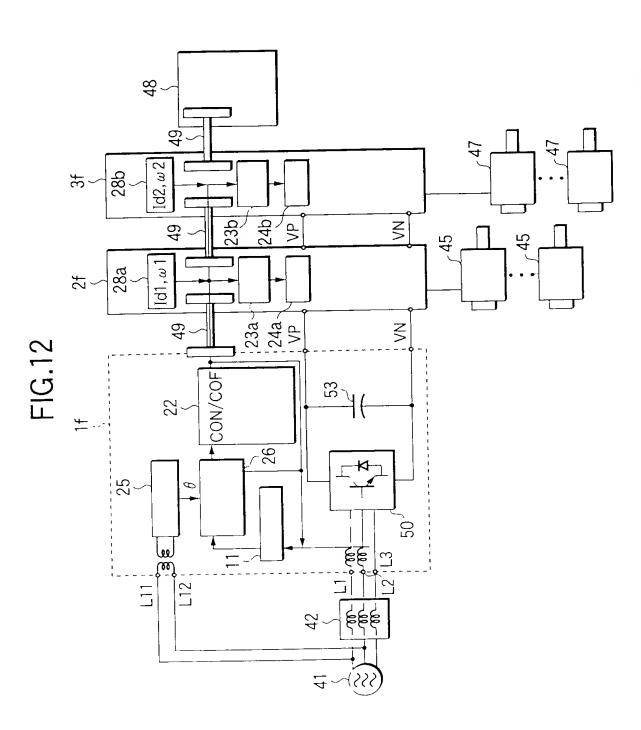
FIG.11(b)

INPUT CURRENT Ii



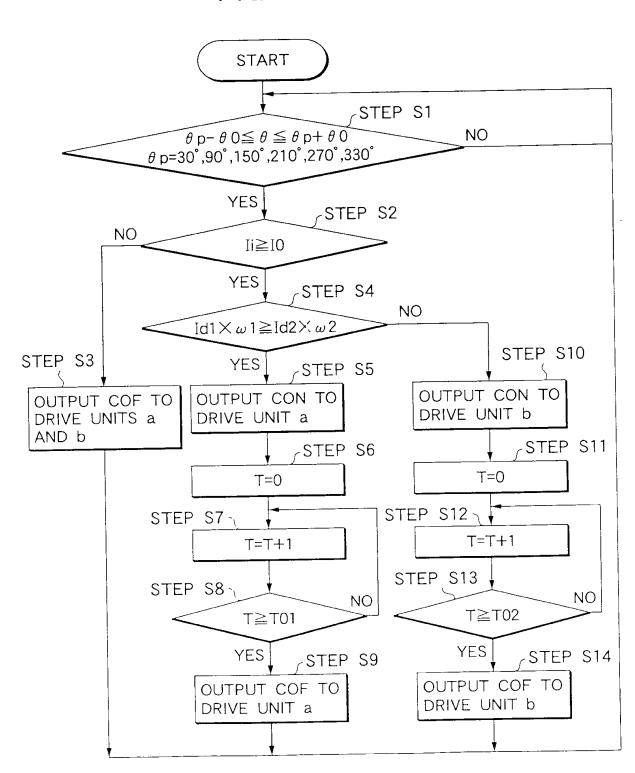
INPUT CURRENT IS DETECTED ONLY IN THE VICINITY OF PHASE WHERE THE INPUT CURRENT REACHES THE PEAK

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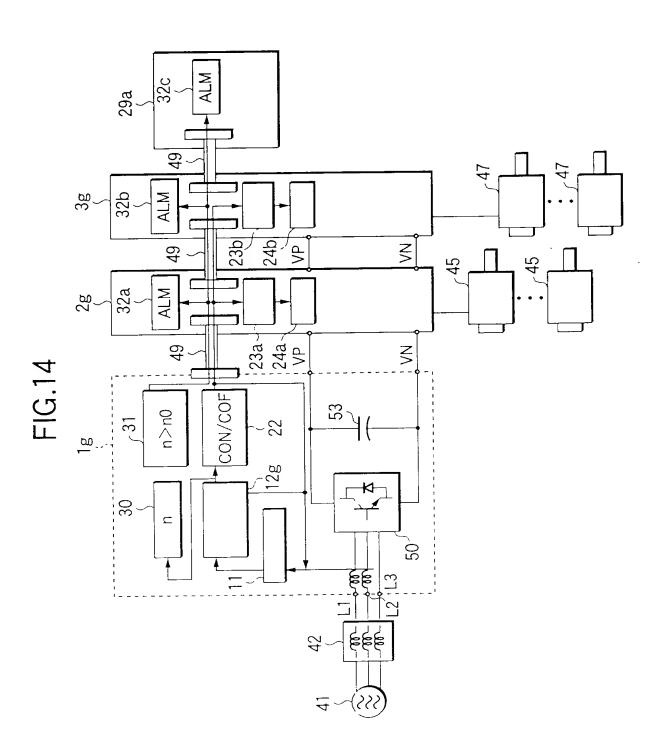
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FIG.13



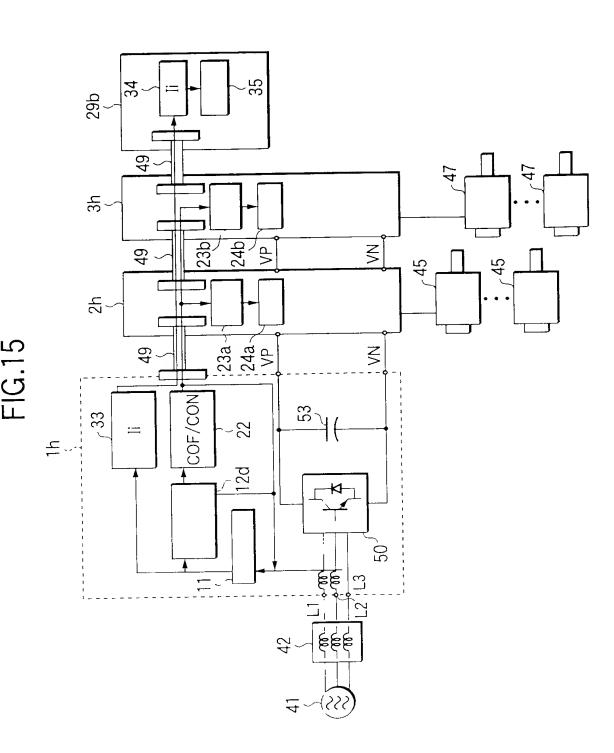
2

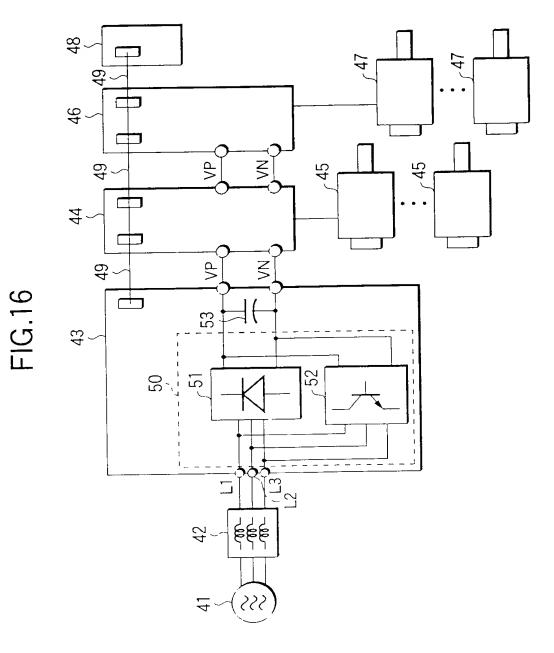
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FIG.17

